

ADX-3981

Release History

Release Version	Comprising:		Dalassa Data	Version	User Manual for this
	Firmware Version	Software Version	Release Date	Details	release (Grass Valley document #)
3.1.2	3.1.1	3.1.2	2015.08.31	(go)	M923-9900-310
3.1.0	3.1.0	3.1.0	2014.04.30	<u>(go)</u>	M923-9900-310

NOTES: The iControl compatibilities shown below are officially supported by Grass Valley. Earlier versions may also work, with bugs or limited features.

The Reference number (Ref#) given for each feature or bug in these Release Notes refers to internal Grass Valley documentation.



UPGRADE PACKAGE: 3.1.2

Firmware version: 3.1.1
Release date: 2015-08-31

iControl compatibility: 5.0 (build 17)

iControl Solo compatibility: 6.0 (build 89)

RCP-200 compatibility: NA

Custom software compatibility: NA

Hardware compatibility: This upgrade package applies to all existing hardware assemblies.

ENHANCEMENTS & NEW FEATURES

	Ref#	Description		
	NDV2091	Loundess is no more an option		
1	<u>ADX3981-</u> <u>94</u>	The loudness feature is not an option anymore. It is given freely. It is the loudness logger software (iControl) that is charged.		

BUGS FIXED IN THIS RELEASE

Ref #	Description		
	Micro-controller crashing when communication is overloaded		
ADX3981- 107	When multiple clients are connected to the Densité frame with a controller of version 212 and later, the XVP can become unresponsive. A bug in the communication engine of the card has been exposed with the higher performance of the newer versions of the Densité controller. WORKAROUNDS:		
	Downgrade the Densité controller of the frame to version 204		
	Preserve incoming VPID		
ADX3981- 96	If all the following conditions are met, the VPID bytes (SMPTE-352) are transferred from the input to the output without any processing: 1. Video format is 3Gbits/s in either Level-A or Level-B Dual-Link 2. Input and output video formats are identical		



<u>001-00-</u> <u>018703</u>

The labels in the iControl UI do not reflect the latest standards/algorithms supported (ITU BS.1770-3). The card does the proper processing but the labels are wrong.

KNOWN BUGS & LIMITATIONS

Ref #	Description
	Metadata packet insertion during 3G Level B outputs.
AMX3981- 14	During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.



UPGRADE PACKAGE: 3.1.0

Firmware version: 3.1.0 Release date: 2014-04-30

iControl compatibility: 5.0 (build 17)

iControl Solo compatibility: 6.0 (build 89)

RCP-200 compatibility: NA

Custom software compatibility: NA

Hardware compatibility: This upgrade package applies to all existing hardware assemblies.

ENHANCEMENTS & NEW FEATURES

Ref#	Description	
ADX3981-	Add support for Dolby Digital Plus encoding.	
91	Support the MOD-DOLBY-ENC-D-2 v5.1 module.	

BUGS FIXED IN THIS RELEASE

Ref #	Description	
	Changing the output selection of an installed module can mute the module's inputs.	
AMX3981- 388	This affected the following modules: - MOD-DOLBY-ENC-D Dolby Digital encoder - MOD-DOLBY-ENC-D-2 Dolby Digital and Dolby Digital Plus encoder - MOD-DOLBY-ENC-E Dolby E encoder - MOD-LA-ALC-x x-channel ALC licensed by Linear Acoustic - MOD-LA-ALC-x-DUP x-channel ALC and upmix licensed by Linear Acoustic - MOD-JA-ALC-x x-channel ALC licensed by Jünger Audio - MOD-JA-ALC-x-DUP x-channel ALC licensed by Jünger Audio and upmix licensed by Linear Acoustic	
ADX3981- 92	Dolby Digital Encoder: pre-encoded (non-PCM) input data corrupted in pass-through mode.	



The sample rate converter (SRC) on the MOD-DOLBY-ENC-D module was always activated thereby modifying and corrupting pre-encoded data passing through the encoder.

KNOWN BUGS & LIMITATIONS

Ref #	Description
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AMX3981- 14	During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.