

ETH3-REF

Release History

Upgrade Package	Comprising:		Release Date	User Manual for this release (Grass Valley document #)
	Firmware Version	Software Version		
2.0.2	2.0.2	2.0.0	2018-05-10	3007-99M00-103 6103-98M02-103
2.0.1	2.0.1	2.0.0	2017-11-27	3007-99M00-103 6103-98M02-103
2.0.0	2.0.0	2.0.0	2017-08-07	3007-99M00-103 6103-98M02-103
1.0.7	1.3.0.528	1.0.7	2016-12-12	3007-99M00-102 6103-98M02-102
1.0.6	1.0.4.522	1.0.6	2016-06-15	3007-99M00-102 6103-98M02-102
1.0.3	1.0.3.501	1.0.3	2016-01-11	3007-99M00-101 6103-98M02-101

NOTES: The iControl compatibilities shown below are officially supported by Grass Valley. Earlier versions may also work, with bugs or limited features.

The Reference number (Ref#) given for each feature or bug in these Release Notes refers to internal Grass Valley documentation.

UPGRADE PACKAGE: 2.0.2

Firmware version: [2.0.2.569](#)

Release date: [2018-05-10](#)

iControl compatibility: [7.10+](#)

iControl Solo compatibility: [7.10+](#)

RCP-200 compatibility: [NA](#)

Hardware compatibility: [This upgrade package applies to all existing hardware assemblies.](#)

ENHANCEMENTS & NEW FEATURES

Ref #	Description
N/A	N/A

BUGS FIXED IN THIS RELEASE

Ref #	Description
CPU3REF-236	Domain number not loaded in PTP config after card rebooting
CPU3REF-240	The densite communication doesn't work during PTP update
CPU3REF-246	The source selection does not match what is displayed After changing the source selection and rebooting the card, iControl and the card edge menu still indicate that the card is locked to the previous source.
DSERV-2721	Sometimes ETHs indicating lock when no PTP is present on the port.
DSERV-2722	Sometimes, setting the PTP alarm level contribution is resulting in IC service message.
DSERV-2723	The PTP ETH1 /ETH2 alarm name with status are not reported to GSM logging.

KNOWN BUGS & LIMITATIONS

Ref #	Description
CPU3REF-235	<p>PTP sometimes not relocking after source interruption. With PTP source set to Auto or ETH#, ETH3-REF has been seen to unlock from the PTP source (as reported from the card edge) although iControl still indicates the Grand Master IP and MAC.</p> <p>WORKAROUND: reboot the ETH3-REF.</p>
DSERV-2687	<p>BASIC Model : ETH3-REF Reference source control can't be set to internal via iControl The Reference source control cannot be set to Internal via iControl; all other menu choices remain unaffected.</p> <p>WORKAROUND: perform the Reference source control configuration via the card edge.</p>

UPGRADE PACKAGE: 2.0.1

Firmware version: [2.0.1.558](#)

Release date: [2017-11-27](#)

iControl compatibility: [7.10+](#)

iControl Solo compatibility: [7.10+](#)

RCP-200 compatibility: [NA](#)

Hardware compatibility: [This upgrade package applies to all existing hardware assemblies.](#)

ENHANCEMENTS & NEW FEATURES

Ref #	Description
	N/A

BUGS FIXED IN THIS RELEASE

Ref #	Description
CPU3REF-226	Hardware Failure 0x02 after upgrading card
CPU3REF-204	ETH3-REF V2.0.0 is unstable in GV Node

KNOWN BUGS & LIMITATIONS

Ref #	Description
CPU3REF-235	<p>PTP sometimes not relocking after source interruption. With PTP source set to Auto or ETH#, ETH3-REF has been seen to unlock from the PTP source (as reported from the card edge) although iControl still indicates the Grand Master IP and MAC.</p> <p>WORKAROUND: reboot the ETH3-REF.</p>
DSERV-2687	<p>ETH3-REF Reference source control can't be set to internal via iControl The Reference source control cannot be set to Internal via iControl; all other menu choices remain</p>

	<p>unaffected.</p> <p>WORKAROUND: perform the Reference source control configuration via the card edge.</p>
<p>CPU3REF-232</p>	<p>Field-upgrading ETH3-REF to V2.0.0 or V2.0.1 causes some cards to periodically relock Some Densité cards have been seen to start periodically relocking after the ETH3-REF is field-upgraded to V2.0.0 or V2.0.1</p> <p>WORKAROUND: After upgrading an ETH3-REF to either V2.0.0 or V2.0.1, perform a single factory reset on it to prevent the periodical relocking of Densité cards in the chassis.</p>

UPGRADE PACKAGE: 2.0.0

Firmware version: [2.0.0.555](#)

Release date: [2017-08-07](#)

iControl compatibility: [7.10+](#)

iControl Solo compatibility: [7.10+](#)

RCP-200 compatibility: [NA](#)

Hardware compatibility: [This upgrade package applies to all existing hardware assemblies.](#)

ENHANCEMENTS & NEW FEATURES

Ref #	Description
CPU3REF-143	Implement SMPTE-2059-1 (based on IEEE-1588) Available only in GV Node and Advanced frame controller.
CPU3REF-144	Implement SMPTE-2059-2 (based on IEEE-1588) Available only in GV Node and Advanced frame controller. The TLV messages are not supported.

BUGS FIXED IN THIS RELEASE

Ref #	Description
DSERV-1750	<p>“Option” tab is missing in the iControl Frame Reference panel</p> <p>The iControl Frame Reference panel does not have an Option tab. This tab is used to activate various options.</p> <p>WORKAROUND: License key can be entered in the frame’s local LCD menu. Press SEL when ACTIVATE NOW is displayed.</p>
CPU3-866	<p>iControl panel does not reflect valid LTC status</p> <p>The UI only displays the LTC1 input validity.</p>

KNOWN BUGS & LIMITATIONS

Ref #	Description
	N/A

UPGRADE PACKAGE: 1.0.7

Firmware version: [1.3.0.528](#)

Release date: [2016-12-12](#)

iControl compatibility: [7.10+](#)

iControl Solo compatibility: [7.10+](#)

RCP-200 compatibility: [NA](#)

Hardware compatibility: [This upgrade package applies to all existing hardware assemblies.](#)

ENHANCEMENTS & NEW FEATURES

Ref #	Description
CPU3-91	Implement 2nd REF input Available only in GV Node and Advanced frame controller.
CPU3-90	Implement 2nd LTC input Available only in GV Node and Advanced frame controller.

BUGS FIXED IN THIS RELEASE

Ref #	Description
CPU3-514	URS not cleanly disabled when Reference option is disabled When the Reference option is disabled, the URS signal distributed in the frame is still active but does not contain synchronization information. The cards will not lock onto the signal but some (mostly audio-only cards like the DAP-1781) still report it as being present in their iControl panel.
CPU3-472	Time Code Logging may skip or repeat the Time Code frame count When Time Code Logging is enabled, there may be frames skipped or repeated in the Densite messages sent to the loggers like the General Status Manager (GSM). Fortunately, they ignore the frame count. The problem is amplified when multiple cards are streaming (thumbnails, H264) in the same frame. The Time Code sent in the URS signal is not affected.

KNOWN BUGS & LIMITATIONS

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CPU3-866	<p>iControl panel does not reflect valid LTC status</p> <p>The UI only displays the LTC1 input validity.</p>

UPGRADE PACKAGE: 1.0.6

Firmware version: [1.0.4.522](#)

Release date: [2016-06-15](#)

iControl compatibility: [7.10+](#)

iControl Solo compatibility: [7.10+](#)

RCP-200 compatibility: [NA](#)

Hardware compatibility: [This upgrade package applies to all existing hardware assemblies.](#)

ENHANCEMENTS & NEW FEATURES

Ref #	Description
CPU3-460	Support of tri-level reference inputs

BUGS FIXED IN THIS RELEASE

Ref #	Description
CPU-686	<p>Upgrade of the ETH3-REF can hang and then fail (rarely) NOTE: This fix can only be applied at the factory.</p> <p>The upgrade of the ETH3-REF (“Frame Reference”) with the iControl Densite Upgrade Manager (DUM) can fail.</p> <p>SYMPTOMS: The upgrade process initiates but hangs (usually at 0.0%) and ultimately times out and fails. At this point, the card is ignoring communications from certain software clients connected to the frame. If one of those software is iControl, its Navigator windows will permanently show the ETH3-REF as “Slot 25 : Empty”. The ETH3-REF looks broken but, in fact, it is not.</p> <p>WORKAROUND:</p> <ul style="list-style-type: none"> - Power cycle the card - Refrain from touching the front LCD touchscreen - On the “Status > Connections” web page of the frame, make sure to have less than 4 connections. If it is not the case, you may have to remove KMX cards from the frame and reboot again. - Restart the upgrade

CPU3-675	<p>Upgrade of the ETH3-REF can render it unusable (very rarely) NOTE: This fix can only be applied at the factory.</p> <p>The upgrade of the ETH3-REF (“Frame Reference”) with the iControl Densite Upgrade Manager (DUM) can fail and brick it (make it unusable).</p> <p>SYMPTOMS: The upgrade process initiates but hangs and becomes unresponsive. Power cycling the card does NOT solve the problem. The card needs to be sent to repair at the factory.</p>
CPU3-474	<p>Audio synchronization in URS not aligned</p>
CPU3-444	<p>Linear Time Code (LTC) pass-through function drops time code packets</p> <p>If the Linear Time Code (LTC) signal at the LTC input “LTC IN” is asynchronous with respect to the reference signal at the REF input “REF IN”, the time code pass-through mode should be activated. This ensures that the incoming time code data is sent in the intra-frame Universal Reference Signal (URS) exactly when received instead of at the beginning of the REF input’s frame. This function is buggy, thus time code packets are dropped.</p> <p>Presently, this function can only be activated using the frame’s local LCD menu: TIME CODE > PROCESSING MODE > PASS-THROUGH.</p>
CPU3-669	<p>The "No Time Code" alarm can be activated when "Time Code source" is set to OFF</p> <p>The “No Time Code” alarm can be activated even though the “Time Code source” is set to OFF and no Time Code signal is detected. This causes the chosen Alarm Level (LED color) to be set.</p>

KNOWN BUGS & LIMITATIONS

Ref #	Description
CPU3-472	<p>Time Code Logging may skip or repeat the Time Code frame count</p> <p>When Time Code Logging is enabled, there may be frames skipped or repeated in the Densite messages sent to the loggers like the General Status Manager (GSM). Fortunately, they ignore the frame count. The problem is amplified when multiple cards are streaming (thumbnails, H264) in the same frame. The Time Code sent in the URS signal is not affected.</p>
CPU3-514	<p>URS not cleanly disabled when Reference option is disabled</p> <p>When the Reference option is disabled, the URS signal distributed in the frame is still active but does not contain synchronization information. The cards will not lock onto the signal but some (mostly audio-only cards like the DAP-1781) still report it as being present in their iControl panel.</p>

DSERV-1750	<p>“Option” tab is missing in the iControl Frame Reference panel</p> <p>The iControl Frame Reference panel does not have an Option tab to enter the Reference Activation key used to enable the URS signal.</p> <p>WORKAROUND: License key can be entered in the frame’s local LCD menu. Press SEL when ACTIVATE NOW is displayed.</p>
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UPGRADE PACKAGE: 1.0.3

Firmware version: 1.0.3.501

Release date: 2016-01-11

iControl compatibility: 7.10+

iControl Solo compatibility: 7.10+

RCP-200 compatibility: NA

Hardware compatibility: This upgrade package applies to all existing hardware assemblies.

ENHANCEMENTS & NEW FEATURES

Ref #	Description
CPU3-67	Initial PUBLIC release of the “basic” CPU-ETH3 for the FR4 frame This is the first official version available for production of the “basic” CPU-ETH3.

BUGS FIXED IN THIS RELEASE

Ref #	Description

KNOWN BUGS & LIMITATIONS

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<p>CPU3-460</p>	<p>Support of tri-level reference inputs not implemented</p>
<p>CPU3-472</p>	<p>Time Code Logging may skip or repeat the Time Code frame count</p> <p>When Time Code Logging is enabled, there may be frames skipped or repeated in the Densite messages sent to the loggers like the General Status Manager (GSM). Fortunately, they ignore the frame count. The problem is amplified when multiple cards are streaming (thumbnails, H264) in the same frame. The Time Code sent in the URS signal is not affected.</p>
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	<p>time code packets are dropped.</p> <p>Presently, this function can only be activated using the frame's local LCD menu: TIME CODE > PROCESSING MODE > PASS-THROUGH.</p>
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