

## EAP-3901/EAP-3101

#### Upgrade Package Release History

Release Version	Comprising:		Delesse Dete	Version	User Manual for this
	Firmware Version	Software Version	Release Date	Details	release (Grass Valley document #)
3.1.4	3.1.3	3.1.3	2019.03.04	<u>(go)</u>	M932-9700-310 (3101) M932-9800-310 (3901)
3.1.3	3.1.2	3.1.3	2017.11.30	<u>(go)</u>	M932-9700-310 (3101) M932-9800-310 (3901)
3.1.2	3.1.1	3.1.2	2015.08.24	<u>(go)</u>	M932-9700-310 (3101) M932-9800-310 (3901)
3.1.1	3.1.0	3.1.1	2014.07.24	<u>(go)</u>	M932-9700-310 (3101) M932-9800-310 (3901)
3.1.0	3.1.0	3.1.0	2014.04.30	<u>(go)</u>	M932-9700-310 (3101) M932-9800-310 (3901)

**NOTES**: The iControl compatibilities shown below are officially supported by Grass Valley. Earlier versions may also work, with bugs or limited features.

The Reference number (Ref#) given for each feature or bug in these Release Notes refers to internal Grass Valley documentation.

### SEE LAST PAGE OF THIS DOCUMENT FOR AN IMPORTANT NOTE ON HARDWARE COMPATIBILITY OF EACH RELEASE



Firmware Version: 3.1.3

**Release date: 2019-03-04** 

iControl compatibility: 5.00+

iControl Solo compatibility: 6.00+

RCP-200 compatibility: 1.80+ (via application server mode)

Hardware compatibility: See note on last page.

#### **BUGS FIXED IN THIS RELEASE**

Ref #	Description
AMX3981-	Support of new flash model
<u>480</u>	A new flash model is used on card and this update supports the hardware change.

Ref #	Description
	Metadata packet insertion during 3G Level B outputs.
AMX3981- 14	During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.



Firmware Version: 3.1.2

**Release date: 2017-11-03** 

iControl compatibility: 5.00+

iControl Solo compatibility: 6.00+

RCP-200 compatibility: 1.80+ (via application server mode)

Hardware compatibility: See note on last page.

#### **BUGS FIXED IN THIS RELEASE**

	Ref #	Description	
	EAP3901- <u>168</u>	GV ALC global config parameters not properly handled	
		"Global Config Mode" parameter changes after re-opening iControl or when loading ALC pgm-1 preset.	

Ref #	Description
	Metadata packet insertion during 3G Level B outputs.
AMX3981- 14	<pre>During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.</pre>



Firmware Version: 3.1.1

**Release date: 2015-08-24** 

iControl compatibility: 5.00+

iControl Solo compatibility: 6.00+

RCP-200 compatibility: 1.80+ (via application server mode)

Hardware compatibility: See note on last page.

#### **ENHANCEMENTS & NEW FEATURES**

-

Ref#	Description		
EAP3901-	Loudness is no more an option		
<u>152</u>	The loudness feature is not an option anymore. It is given freely. It is the loudness logger software (iControl) that is charged.		

#### **BUGS FIXED IN THIS RELEASE**

Ref #	Description
<u>DSERV-</u> <u>1459</u>	Dolby DEC card not properly detected When used with DOLBY-DEC-2 modules, the decoder is greyed out in the UI. Can't select the decoder as a metadata source.
<u>EAP3901-</u> <u>151</u>	<pre>Micro-controller crashing when communication is overloaded When multiple clients are connected to the Densité frame with a controller of version 212 and later, the XVP can become unresponsive. A bug in the communication engine of the card has been exposed with the higher performance of the newer versions of the Densité controller. WORKAROUNDS: • Downgrade the Densité controller of the frame to version 204</pre>
<u>EAP3901-</u> <u>154</u>	<b>Preserve incoming VPID</b> If all the following conditions are met, the VPID bytes (SMPTE-352) are





transferred from the input to the output without any processing:
1. Video format is 3Gbits/s in either Level-A or Level-B Dual-Link 2. Input and output video formats are identical
Updated Algorithms for audio loudness and true-peak level (ITU BS.1770-3)

Ref #	Description
	Metadata packet insertion during 3G Level B outputs.
AMX3981- 14	During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.



Firmware Version: 3.1.0

Release date: 2014-07-24

iControl compatibility: 5.00+

iControl Solo compatibility: 6.00+

RCP-200 compatibility: 1.80+ (via application server mode)

Hardware compatibility: See note on last page.

#### **ENHANCEMENTS & NEW FEATURES**

Ref#	Description

#### **BUGS FIXED IN THIS RELEASE**

Ref #	Description
DSERV-	Scripting does not work in iControlWeb
561	With previous upgrade packages installed, it is not possible to control the service using the proxy from iControlWeb. A java error is reported and scripting is not supported.

F	Ref #	Description
ΔΜ	AMX3981- 14	Metadata packet insertion during 3G Level B outputs.
		During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources



with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.



# **Release Notes**

## **UPGRADE PACKAGE: 3.1.0**

Firmware Version: 3.1.0

Release date: 2014-04-30

iControl compatibility: 5.00+

iControl Solo compatibility: 6.00+

RCP-200 compatibility: NA

Hardware compatibility: See note on last page.

#### **ENHANCEMENTS & NEW FEATURES**

Ref#	Description
EAP3981- 142	Add support for Dolby Digital Plus encoding.
	Support the MOD-DOLBY-ENC-D-2 v5.1 module.

#### **BUGS FIXED IN THIS RELEASE**

Ref #	Description
	Changing the output selection of an installed module can mute the module's inputs.
AMX3981- 388	<pre>This affected the following modules:</pre>
EAP3981- 143	Dolby Digital Encoder: pre-encoded (non-PCM) input data corrupted in pass- through mode.
	The sample rate converter (SRC) on the MOD-DOLBY-ENC-D module was always activated thereby modifying and corrupting pre-encoded data passing through



the encoder.

Ref #	Description
	Metadata packet insertion during 3G Level B outputs.
AMX3981- 14	During 3G Level B operation, there may be situations where output Ancillary Time Code (ATC) packets will be inserted on Link B. Asynchronous sources with respect to the reference may insert ATC packets on Link B instead of Link A. If the source and reference are synchronous, then ATC packets may find themselves on Link B depending on the source's timing with respect to the reference. An aligned input places ATC packets on the proper link (Link A). This situation does not occur when no reference is installed.



## HARDWARE COMPATIBILITY

The assembly based on PCB Revision 1922-0100-300 introduces a new dataflash model, supported by release 3.1.4 (firmware: 3.1.3) and later. While release 3.1.4 can be installed on assemblies using older PCB revisions, older releases are not compatible with PCB revision 1922-0100-300.

PCB Revision	Compatible Release / Firmware
<mark>1922-0100-300</mark>	Requires release 3.1.4 (Firmware: 3.1.3) or later
1922-0100-200	Compatible with all releases
1922-0100-100	Compatible with all releases

See below for location of the PCB revision number

